FS-00-001

April 20, 2005

To: Commissioner for Patents
    P.O. Box 1450
    Alexandria, VA 22313-1450

Fr: George O. Saile, Reg. No. 19,572
    28 Davis Avenue
    Poughkeepsie, N.Y. 12603

Subject: | Serial No.   10/041,044 01/09/02 |

                            Y.C. Lim

                            NEW FILTER BANK FOR GRAPHICS
                            EQUALIZER IMPLEMENTATION
                            |___   ___|

SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

Enclosed is Form PTO-1449, Information Disclosure Citation

In An Application.

The following Patents and/or Publications are submitted to

comply with the duty of disclosure under CFR 1.97-1.99 and

37 CFR 1.56.

CERTIFICATE OF MAILING

I hereby certify that this correspondence is being
deposited with the United States Postal Service as first class
mail in an envelope addressed to: Commissioner for Patents,
P.O. Box 1450, Alexandria, VA 22313-1450, on April 21, 2005.

04/27/2005 GWORDOF1 00000017 190033 10041044   Stephen B. Ackerman, Reg.# 37761
01 FC:1806   180.00 DA

                Signature/Date _SBA_ 4/21/05

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FS-00-001

This Information Disclosure Statement is being filed more than three months after the U.S. filing date and after the mailing date of the first Office Action on the merits, but before the mailing date of a Final Action under 1.113 or Notice of Allowance under 1.311 (37CFR 1.97(c)).

The Commissioner is hereby authorized to charge the IDS Processing Fee of $180, under 37 CFR 1.17(p), to Deposit Account No. 19-0033. A duplicate copy of this sheet is enclosed.

Saeed Vaseghi, Chapter 5, Digital Filters (online), Brunel University, West London, Dec. 17, 2001, (retrieved on Nov. 11, 2004), Retreved from The Internet:<URL: http://www.brunel.ac.uk/depts/ee/Research_Programme/COM/Home_ Saeed_Vaseghi/, pages 7-11, 44, 46, 47, 57, introduces the general form of the equation for a linear time-invariant filter and considers the various methods of description of a filter in the time and frequency domains.

U.S. Patent 4,125,900 to Betts, "Cascaded Recursive Digital Filter," discusses cascaded filters, particularly cascaded recursive digital filters.

U.S. Patent 5,210,806 to Kihara et al, "Digital Audio Signal Processing Apparatus," discloses a digital audio signal processing apparatus of a type wherein a graphic equalizer including a plurality of filters connected in series to one another is subjected to arithmetic operation processing to be defined to thereby output the result of its processing as data therefrom.

Sincerely,

Stephen B. Ackerman,
Reg. No. 37761

Form PTO-1449

INFORMATION DISCLOSURE CITATION
IN AN APPLICATION
(Use several sheets if necessary)

APR 2 5 2005 (stamp)

Docket Number (Optional)	Application Number
FS-00-001	10/041,044

Applicant: Y. C. Lim

Filing Date	Group Art Unit
01/09/02	

U. S. PATENT DOCUMENTS

EXAMINER INITIAL	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
	4125900	11/14/78	Betts	364	724	7/1/77
	5210806	5/11/93	Kihara et al.	381	103	10/16/90

FOREIGN PATENT DOCUMENTS

EXAMINER INITIAL	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	Translation YES	Translation NO

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

Saeed Vaseghi, Chapter 5, Digital Filters (online), Brunel University, West London, 12/17/01, (retrieved on 11/11/01), from The Internet: < URL:
http://www.brunel.ac.uk/depts/ee/Research_Programme/com/Home_Saeed_Vaseghi/, pp. 7-11, 44,46, 47, 57.

EXAMINER	DATE CONSIDERED

EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP § 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to the applicant.